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| --- | --- | --- | --- |
| **pbs Project Status (07/22/2025 - 16:22:02)** | | | |
| **Project File:** | pbs.xise | **Parser Errors:** | No Errors |
| **Module Name:** | pbs | **Implementation State:** | Programming File Generated |
| **Target Device:** | xc3s400-4ft256 | * **Errors:** | No Errors |
| **Product Version:** | ISE 14.6 | * **Warnings:** | [1 Warning (1 new)](D://ms/pbs/_xmsgs/*.xmsgs?&DataKey=Warning) |
| **Design Goal:** | Balanced | * **Routing Results:** | [All Signals Completely Routed](D://ms/pbs/pbs.unroutes) |
| **Design Strategy:** | [Xilinx Default (unlocked)](Xilinx%20Default%20(unlocked)?&DataKey=Strategy) | * **Timing Constraints:** |  |
| **Environment:** | [System Settings](D://ms/pbs/pbs_envsettings.html) | * **Final Timing Score:** | 0  [(Timing Report)](D://ms/pbs/pbs.twx?&DataKey=XmlTimingReport) |

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| **Device Utilization Summary** | | | | | [**[-]**](?&ExpandedTable=DeviceUtilizationSummary) |
| **Logic Utilization** | **Used** | **Available** | **Utilization** | **Note(s)** | |
| Number of 4 input LUTs | 7 | 7,168 | 1% |  | |
| Number of occupied Slices | 4 | 3,584 | 1% |  | |
| Number of Slices containing only related logic | 4 | 4 | 100% |  | |
| Number of Slices containing unrelated logic | 0 | 4 | 0% |  | |
| Total Number of 4 input LUTs | 7 | 7,168 | 1% |  | |
| Number of bonded [IOBs](D://ms/pbs/pbs_map.xrpt?&DataKey=IOBProperties) | 13 | 173 | 7% |  | |
| Average Fanout of Non-Clock Nets | 1.80 |  |  |  | |

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| --- | --- | --- | --- | --- |
| **Performance Summary** | | | | [**[-]**](?&ExpandedTable=PerformanceSummary) |
| **Final Timing Score:** | 0 (Setup: 0, Hold: 0) | **Pinout Data:** | [Pinout Report](D://ms/pbs/pbs_par.xrpt?&DataKey=PinoutData) | |
| **Routing Results:** | [All Signals Completely Routed](D://ms/pbs/pbs.unroutes) | **Clock Data:** | [Clock Report](D://ms/pbs/pbs_par.xrpt?&DataKey=ClocksData) | |
| **Timing Constraints:** |  |  |  | |